

FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)								ATTY. DOCKET NO. YOR920010675		SERIAL NO.:			
								APPLICANT: Michael K. Gschwind et al.					
								FILING DATE: Herewith		GROUP:			
REFERENCE DESIGNATION				U.S. PATENT DOCUMENTS									
EXAMINER INITIALS		DOCUMENT NUMBER		DATE		NAME		CLASS	SUBCLASS	FILING DATE (IF APPRO.)			
(Csk)	AA	5	4	2	3	0	5	1	06/06/1995	R. A. Fuller et al	709	395	09/24/1992
(Csk)	AB	6	1	9	5	7	4	6	02/27/2001	R. K. Nair	712	217	01/31/1997
(Csk)	AC	5	7	5	8	1	7	6	05/26/1998	R. C. Agarwal et al.	395	800	09/28/1994
	AD												
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	AH												
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FOREIGN PATENT DOCUMENTS													
		DOCUMENT NUMBER		DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION					
								YES	NO				
	AL												
	AM												
	AN												
	AO												
	AP												
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)													
(Csk)	AQ	M. Tremblay et al. "VIS Speeds New Media Processing", IEEE Micro, 8/1996 pp. 10-22											
(Csk)	AR	R. Lee, "Multimedia Enhancements for PA-RISC Processors" Hewlett-Packard Company, HotChips VI, 8/94 pp. 7.3.1-7.3.10 (183-191)											
(Csk)	AS	K. Diefendorff et al. "How Multimedia Workloads Will change Processor Design", Computer, 9/1997 pp. 43-45											
EXAMINER		<i>Ken S. Kim</i>		DATE CONSIDERED		3-26-04							
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.													

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	BA						
	BB						
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	BL						
	BM						
	BN						
	BO						
	BP						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>(CSK)</i>	BQ		T. M Conte et al. "Challenges to Combining General-Purpose and Multimedia Processors" 12/1997, Computer IEEE pp.33-37				
<i>(CSK)</i>	BR		R. B. Lee " Subword Parallelism with MAX-2" Hewlett Packard IEEE Micro, 8/1996 pp.51- 59				
<i>(CSK)</i>	BS		A. Peleg et al. "MMX Technology Extension to the Intel Architecture" IEEE Micro 8/1996 pp. 42-50				

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	CB							
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	CH							
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	CJ							
	CK							

FOREIGN PATENT DOCUMENTS								
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								YES NO
	CL							
	CM							
	CN							
	CO							
	CP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>CSK</i>	CQ		A. Peleg et al., "Intel MMX for Multimedia PCs" Communications of the ACMM Jauary 1997/Vol. 40, No. 1 pp. 25-38				
	CR						
	CS						

EXAMINER	<i>Ken S. Klein</i>	DATE CONSIDERED	<i>3-26-04</i>
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